

WHAT IS CLAIMED IS:

1. An information processing apparatus for translating a source file including a dynamic variable into an object file by a compiling process and converting the object file into an executable load module by a linking process, the apparatus comprising:

dynamic variable specifying means for specifying a target dynamic variable from the source file;

area specifying means for specifying areas ensured in the case of a dynamic variable specified by the dynamic variable specifying means being developed into a memory at the time of executing the load module; and

initializing means for initializing areas specified by the area specifying means to a predetermined value.

2. The information processing apparatus according to claim 1, further comprising area integrating means for integrating at need areas ensured in the case of a plurality of different dynamic variables specified by the dynamic variable specifying means being developed into the memory.

3. The information processing apparatus according to claim 1, further comprising variable integrating means for integrating the same dynamic variables dispersed in one or more object files integrated by the linking process.

4. The information processing apparatus according to claim 1, wherein the dynamic variable specifying means allocates a target dynamic variable to a new data section, further wherein the initializing means performs an
5 initializing process on a dynamic variable alone allocated to the new data section.

5. The information processing apparatus according to claim 4, wherein when a plurality of object files are
10 linked to generate the load module, the initializing means does not perform an initializing process on a dynamic variable which is allocated to the new data section in a predetermined object file and is allocated to an ordinary data section in another object file.

6. The information processing apparatus according to claim 1, further comprising initial-value entering means for entering an initial value used by the initializing means before a compiling process.
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7. The information processing apparatus according to claim 1, further comprising initial-value entering means for entering an initial value used by the initializing means before executing the load module.
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8. The information processing apparatus according to claim 1, further comprising initialized variable
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specifying means for specifying a dynamic variable on which the initializing process is performed.

9. The information processing apparatus according to claim 1, further comprising error informing means for informing of an error in the case of a dynamic variable which holds the initial value being referred to at the time of executing the load module.

10. The information processing apparatus according to claim 9, further comprising memory ensuring means for ensuring memory areas being a predetermined number more than the number of elements of an array declared in source code if the dynamic variable is an array, wherein the initializing means initializes all areas in a memory ensured by the memory ensuring means to a predetermined value, further wherein the error informing means informs of an error in the case of an array which holds the initial value being referred to.

11. A computer-readable record medium recording a computer program for translating a source file including a dynamic variable into an object file by a compiling process and converting the object file into an executable load module by a linking process, the program comprising the functions of:

dynamic variable specifying means for specifying a

target dynamic variable from the source file;

area specifying means for specifying areas ensured
in the case of a dynamic variable specified by the dynamic
variable specifying means being developed into a memory at
5 the time of executing the load module; and

initializing means for initializing areas specified
by the area specifying means to a predetermined value.

12. An information processing apparatus for
10 translating a source file including an array into an object
file by a compiling process and converting the object file
into an executable load module by a linking process, the
apparatus comprising:

array specifying means for specifying a target
15 array from the source file;

area ensuring means for ensuring, at the time of
executing the load module, areas in a memory being a
predetermined number of bytes more than areas declared in
an array specified by the array specifying means; and

20 initializing means for initializing areas ensured
by the area ensuring means to a predetermined value.